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9-27-02

PATENT
Docket Number: 16356.737 (DC-02355)
Customer No.: 000027683

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Gary J. Verdun

Serial No. 09/707,585

Filed: November 6, 2000

For: PROCESSOR POWER STATE
TRANSITIONS USING SEPARATE
LOGIC CONTROL

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Examiner: Unassigned

Group Art Unit: 2182

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Technology Center 2100

Box Non-Fee Amendment
Commissioner For Patents
Washington, DC 20231

Sir:

Prior to examination, please amend the above-identified patent application as follows:



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TRANSMITTAL

Prior to examination, enclosed are the following regarding the above-identified patent application:

1. Preliminary Amendment;
2. Return postcard; and
3. Transmittal letter (in duplicate).

The Commissioner is hereby authorized to charge payment of any fees associated with any of the papers submitted herewith to Deposit Account No. 08-1394.

Respectfully submitted,


James R. Bell
Registration No. 26,528

Dated: 9-12-02
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Dallas, Texas 75202-3789
Telephone: 512/867-8407
Facsimile: 512/867-8470

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, Washington, D.C. 20231

on 9/12/02

Date MJR

Signature Nishit Pasareya

Typed/Printed name of person signing Certificate Nishit Pasareya